

Speaker Biography: Moshe Gavrielov

January 2018 Onward: Various Active BOD/Committee Roles

Compensation Committee Member of TSMC BOD

Executive Chairman of the BOD, Wind River: a TPG Capital owned leader in embedded software for intelligent embedded systems.

Member BOD of Foretellis: a unique technology leading SW startup focused on enabling safe autonomous vehicles with GigaScale intelligent verification.

January 2008-January 2018: President and CEO, Xilinx

Drove Xilinx through a broad strategic and operational transformation resulting in a significant market cap expansion approaching a valuation of \$20B. This was done by broadening the company's scope beyond its FPGA roots and evolving into the industry's leading All Programmable player with a significantly broader technology portfolio. Accomplishments include delivering three consecutive generations of Silicon/SW/IP technology leadership, significant market share gains, 9 consecutive quarters of revenue growth, while delivering 70% GM and 30% OM. These strategic, operational and financial accomplishments drove Xilinx stock up by 4X during my tenure.

April 2005-November 2007: EVP, Cadence Design Systems

EVP and General Manager of Cadence's Verification Division post the Verisity acquisition reporting to the CEO. Responsible for Cadence's \$300M verification business (SW simulation, HW emulation, and advanced verification methodology/infrastructure) which was the fastest growing business at Cadence during my tenure.

March 1998-April 2005: CEO, Verisity

Brought in as CEO by the venture investors in March 1998. Drove the company's emergence from a small private entity, through a successful NASDAQ IPO in March 2001, delivering ongoing very profitable top and bottom-line growth leading to the eventual acquisition by Cadence for \$315M in April 2005.

October 1988-March 1998: EVP, LSI Logic

Numerous roles with growing scope of responsibility leading up to EVP of the Products Group comprised of all engineering and marketing of LSI Logic's \$1.3B revenue business reporting to the CEO. These executive roles included engineering, marketing, GM and international sales.

July 1985-October 1988: Consulting Engineer, Digital Equipment Corporation

Project manager and architect of a high-performance floating-point unit implemented in CMOS technology.

September 1979-June 1985: Engineering/Project Management, National Semiconductor

Various roles in high performance processor design in first job out of college.

Education:

1975-1983: Technion – Israel Institute of Technology

BSEE and MSCS